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EL844055910

GP 3724

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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	09/687,600
	Filing Date	October 12, 2000
	First Named Inventor	Jason E. Tripard
	Group Art Unit	3724
	Examiner Name	S. Choi
Total Number of Pages in This Submission	12	Attorney Docket Number MI22-1550

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	D. Brent Kenady Reg. No. 40,045; Wells, St. John, et al.
Signature	
Date	12-26-01

CERTIFICATE OF MAILING		
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 12-26-01		
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844055910

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/687,600  
Filing Date .... October 12, 2000  
Inventor .... Jason Tripp  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 3724  
Examiner .... S. Choi  
Attorney's Docket No. .... MI22-1550  
Title: Integrated Circuit Package Separators

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**RESPONSE TO SEPTEMBER 25, 2001 OFFICE ACTION**

To: Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)  
Wells, St. John, Roberts, Gregory & Matkin P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

**AMENDMENTS**

**In the Specification**

Replace the ABSTRACT with the following:

A base having a plurality of pins extending upwardly therefrom is provided.  
A support is provided over the base. The support has an upper surface and a plurality of holes extending therethrough. The pins extend through the holes and upwardly beyond the upper surface of the support. An actuator is provided beneath the support. A board having a plurality of integrated circuits bonded thereto is provided. The integrated circuits form a repeating pattern of integrated circuit packages across the board, and the board has a plurality of holes